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Morrish

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
PATENT APPLICATION

Application No.: 09/682,131 ✓
 Applicant: Fujio et al.
 Filing Date: July 25, 2001 ✓
 Title: SEMICONDUCTOR INTEGRATED
 CIRCUIT DEVICE WITH EMI
 PREVENTION STRUCTURE

Atty. Docket: JP9-2000-0229
 Today's Date: March 20, 2003
 Examiner: Chris C. Chu
 Group Art Unit: 2815
 Fax: 703-872-9318

Amendment Under 37 CFR 1.111

FAX RECEIVED

Amendment C

MAR 20 2003

Honorable Commissioner of Patents and Trademarks
 Washington, D.C. 20231

TECHNOLOGY CENTER 2800

Sir:

In response to the Office Action dated December 30, 2002, Applicants respectfully request reconsideration of the outstanding rejection of claims in view of the amendments and remarks that follow. If the PTO determines that a fee is due, please charge Applicants' deposit account, 09-0456.

CERTIFICATE OF MAILING

I hereby certify that, on the date shown below, this correspondence is being:

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deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Assistant Commissioner of Patents, Washington, DC 20231.

Date: 03/20/2003

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ANTHONY J. CANALE
 Name
Anthony J. Canale
 Signature

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